

FIG. 1

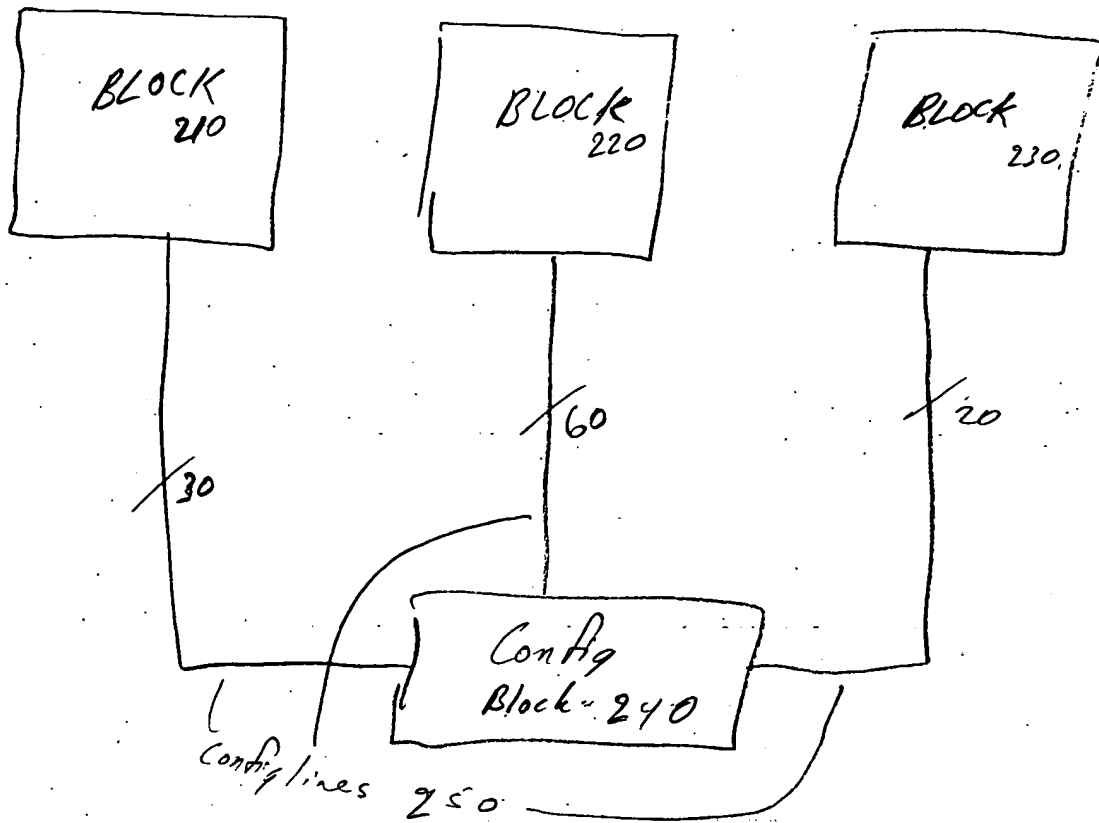
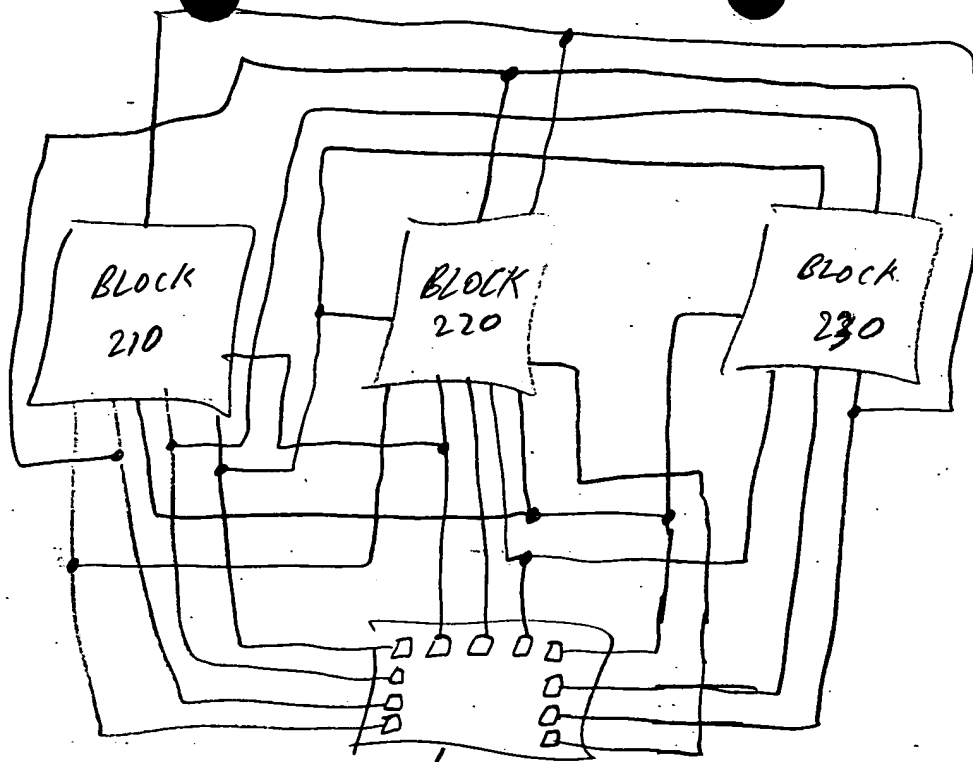


Fig. 2A
(Prior Art)

(fig. 1 of Eric's disclosure)



Config
BLOCK
240

Fig. 2B

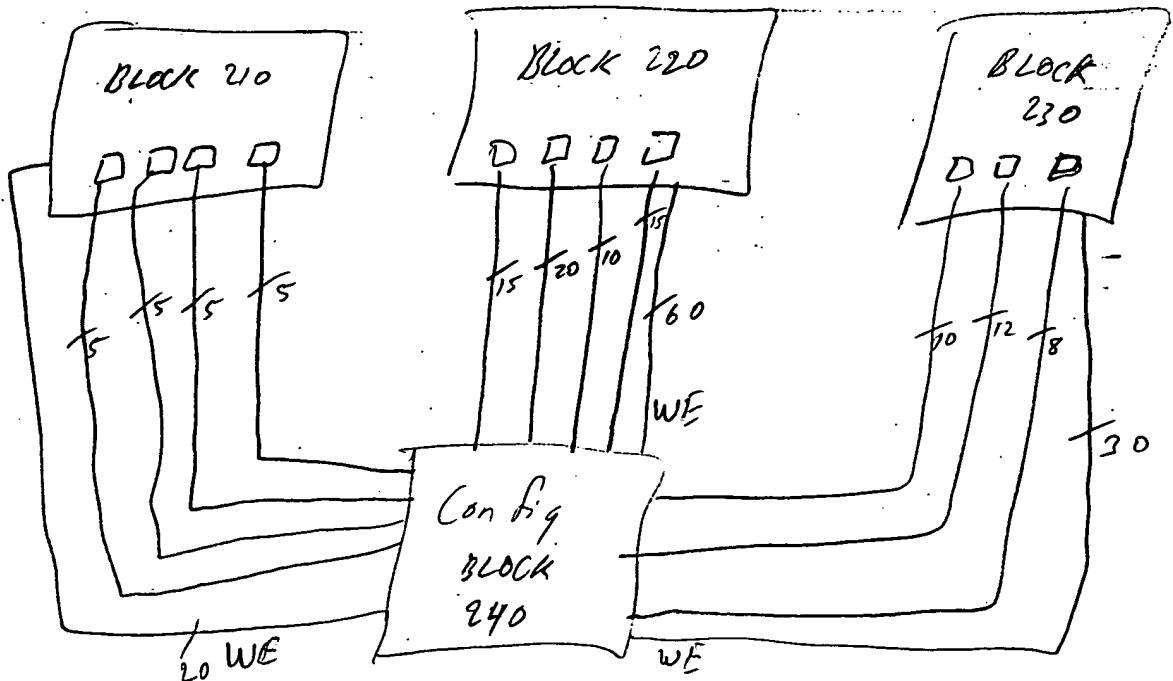


Fig. 2C

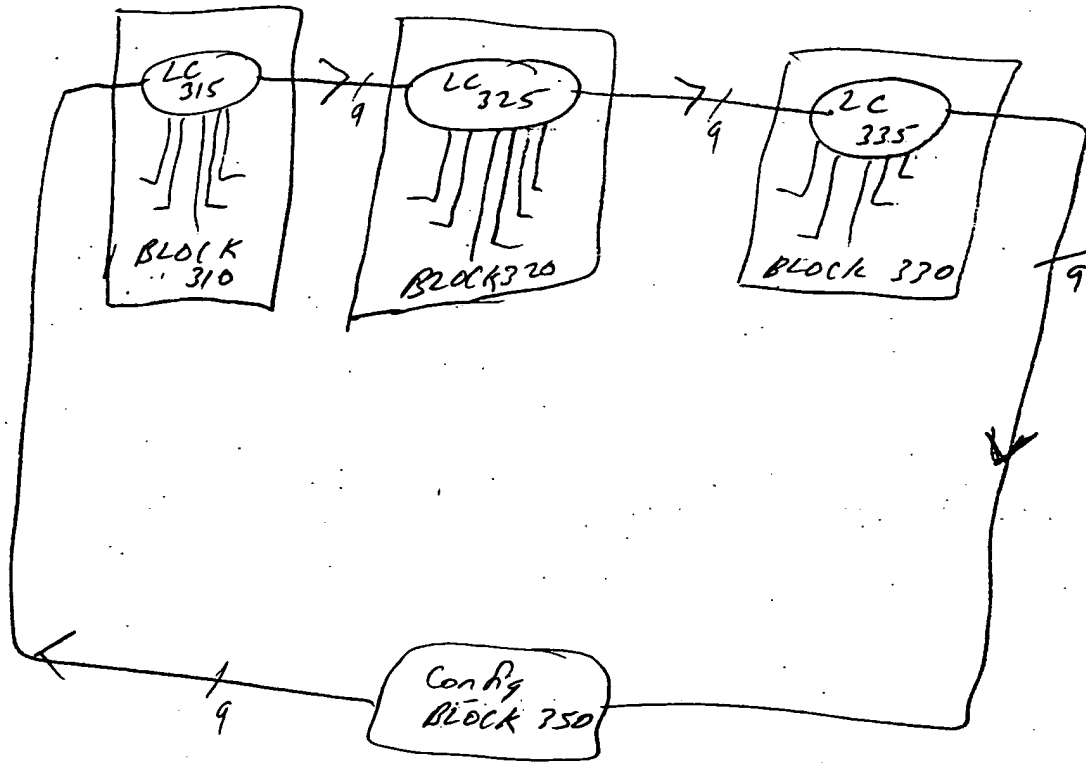


Fig. 3

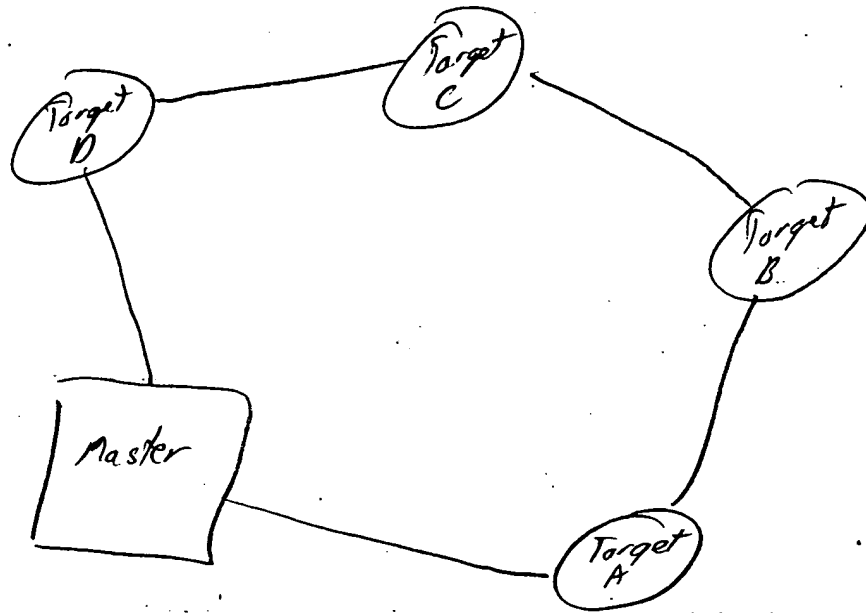


Fig. 4A

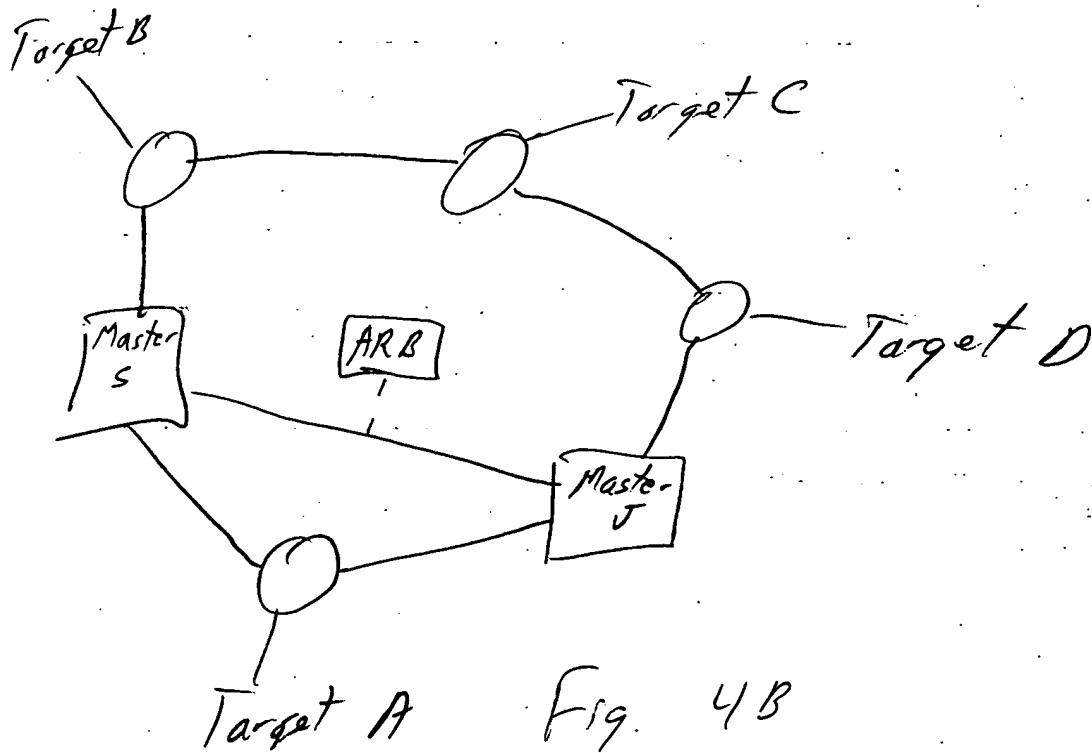


Fig. 4B



6060-34582-0

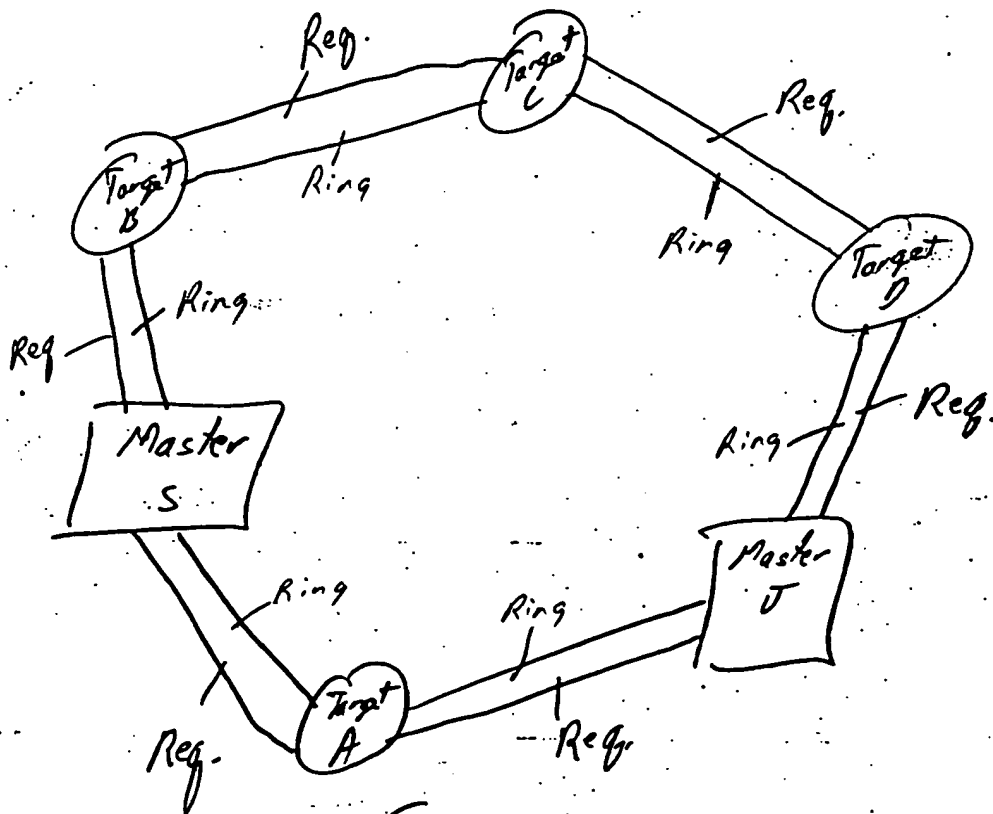


Fig. 4C

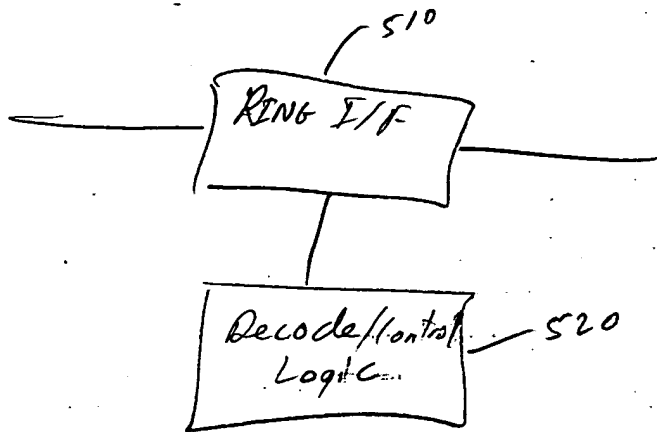


Fig. 5A

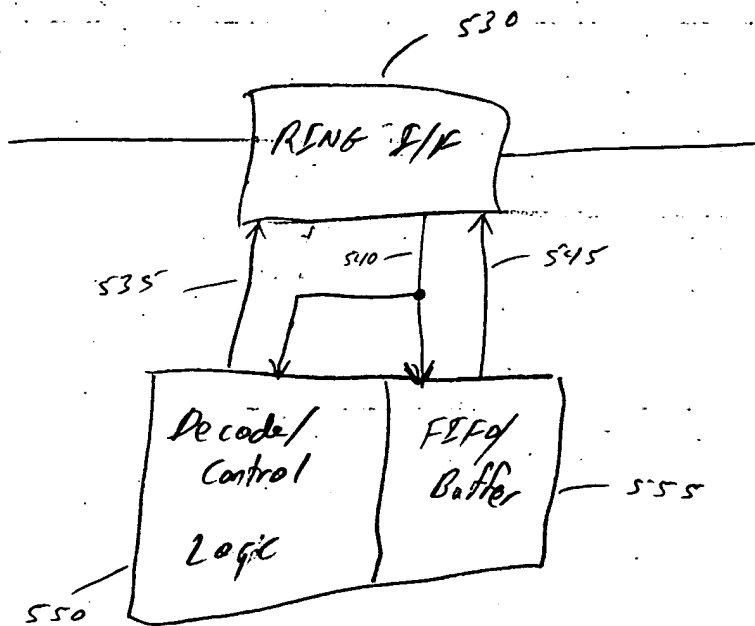


Fig. 5B



Fig. 6A

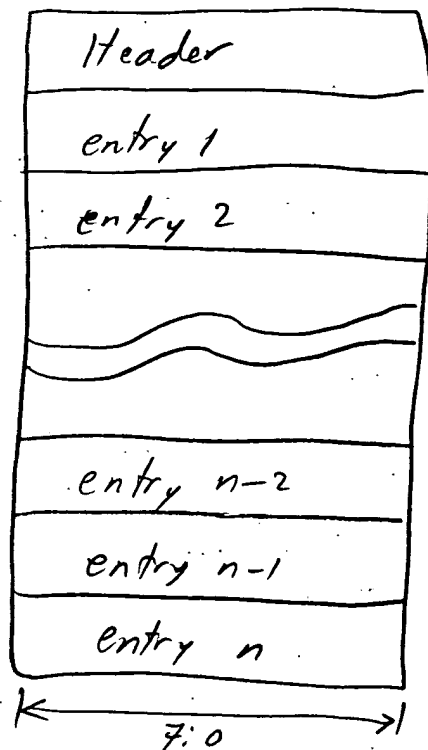


Fig. 6B

The diagram illustrates packet scheduling. It shows a timeline with vertical lines representing time slots. A 'Valid' period is marked at the top. Below it, a 'Packet' is shown as a sequence of segments labeled A0, A1, A2, A3, D0, D1, D2, D3. The segments are connected by diagonal lines, indicating they are part of the same packet. The timeline is divided into time slots labeled $t_0, t_1, t_2, t_3, t_4, t_5, t_6, t_7, t_8, t_9, t_{10}$. The packet starts at t_0 and ends at t_{10} .

Fig. 7

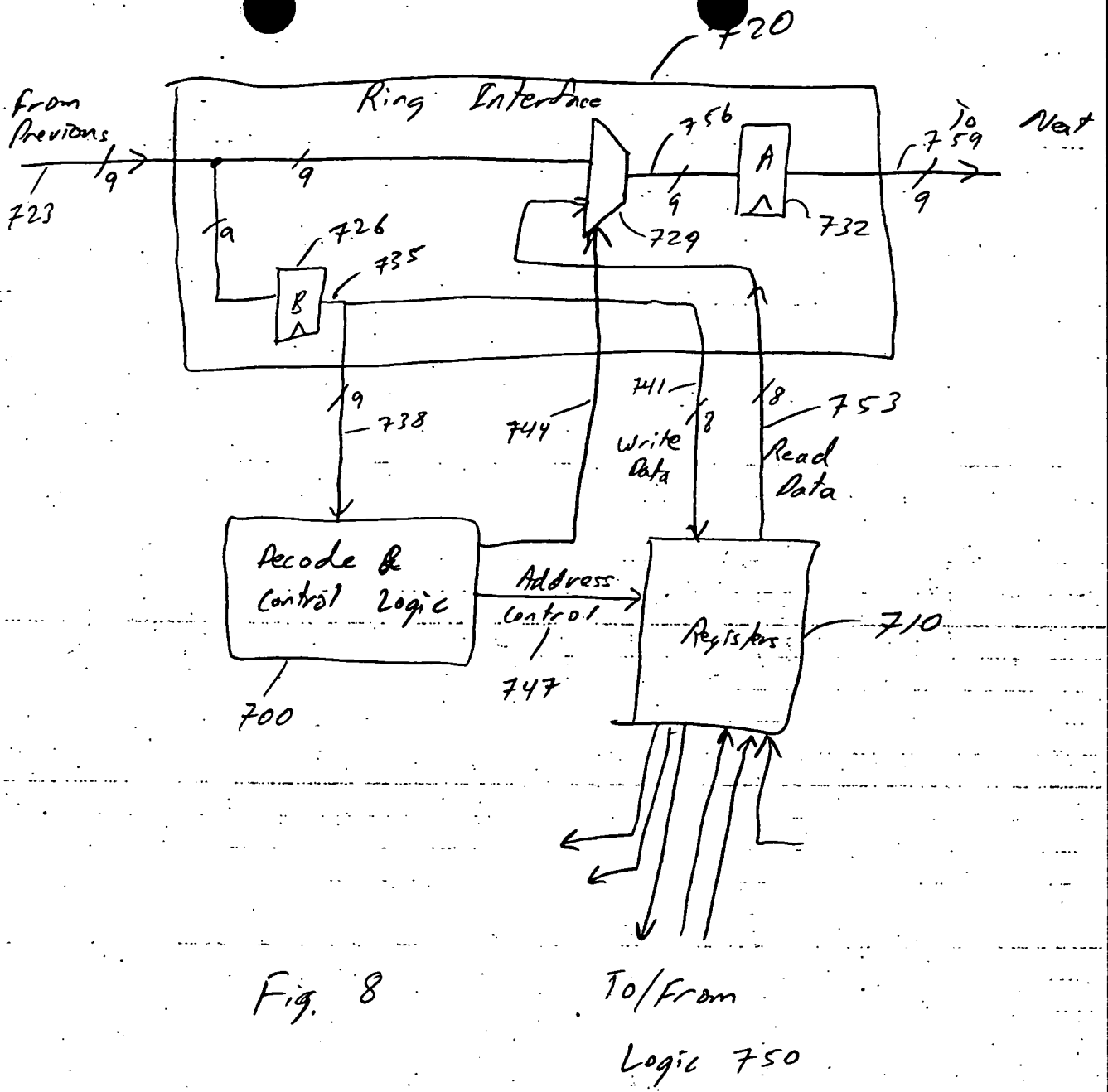
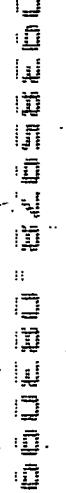
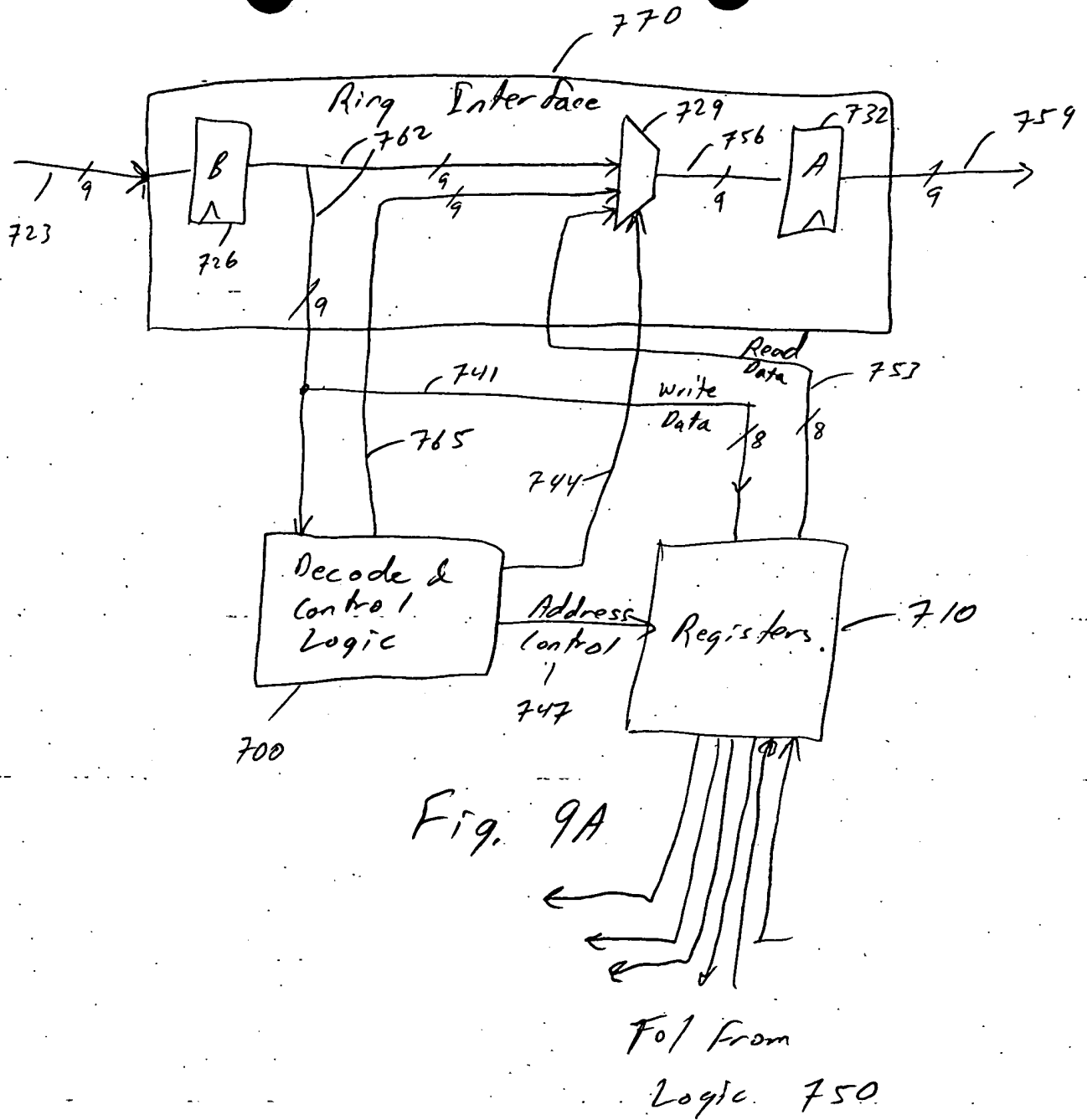


Fig. 8





22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

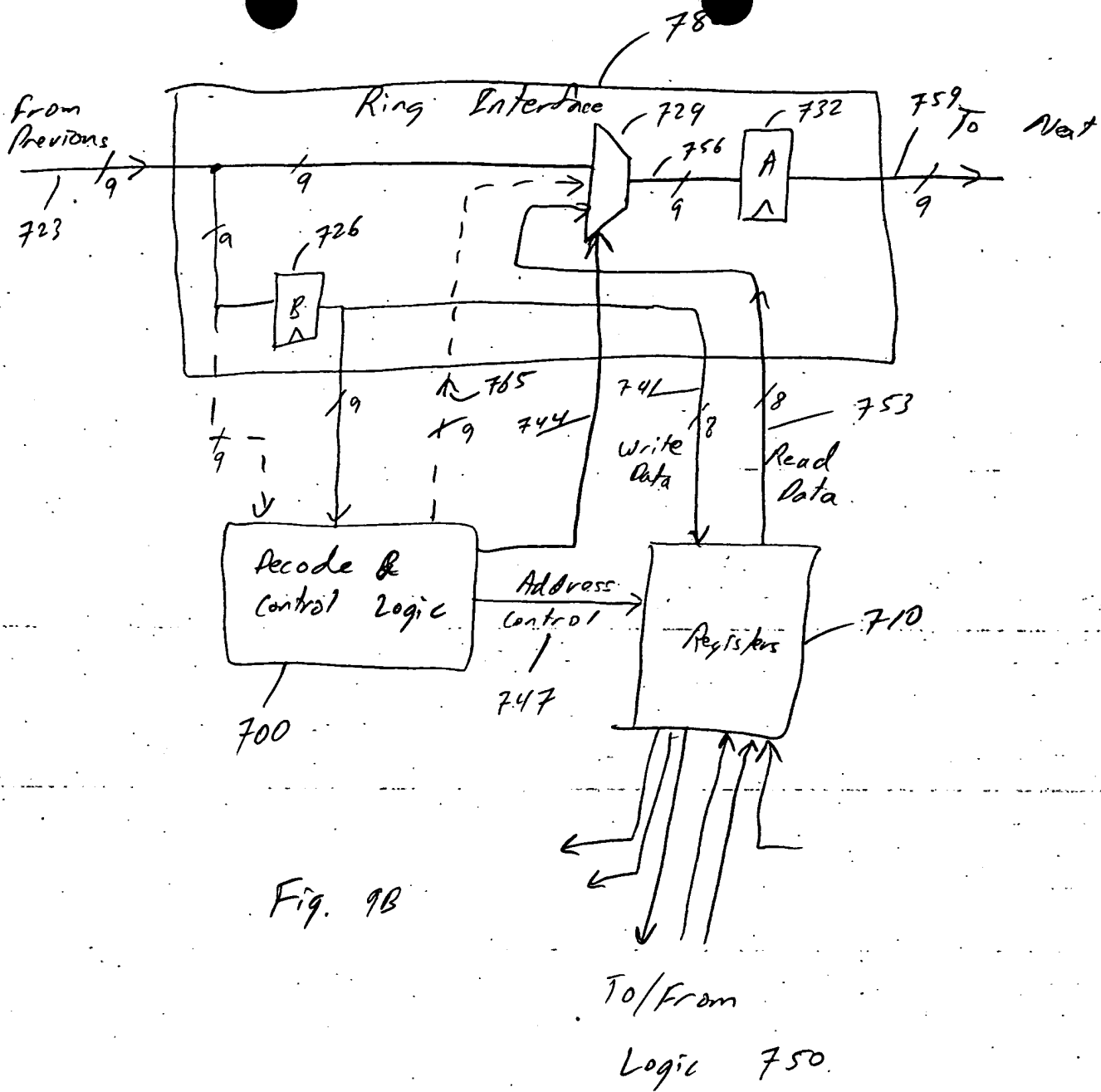


Fig. 9B

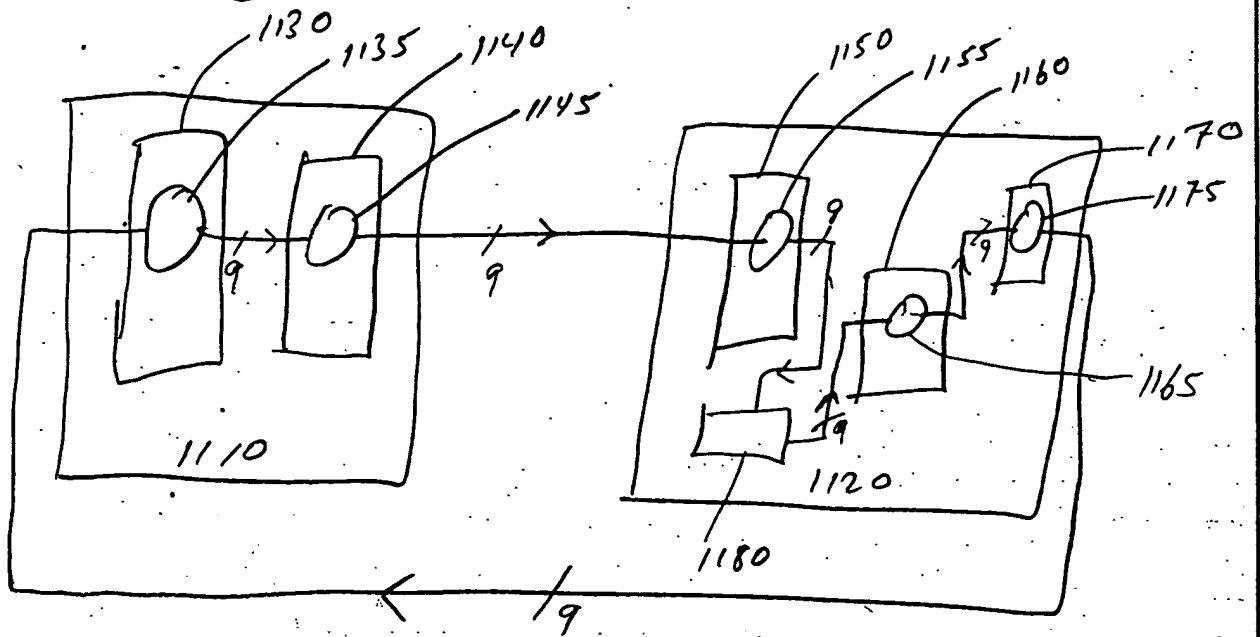


Fig. 11.

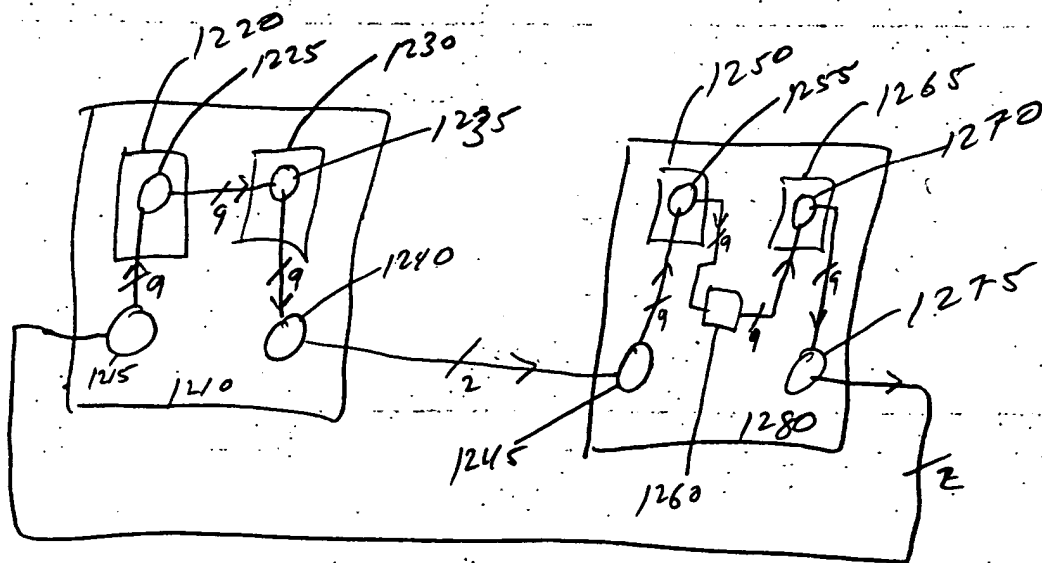


Fig. 12

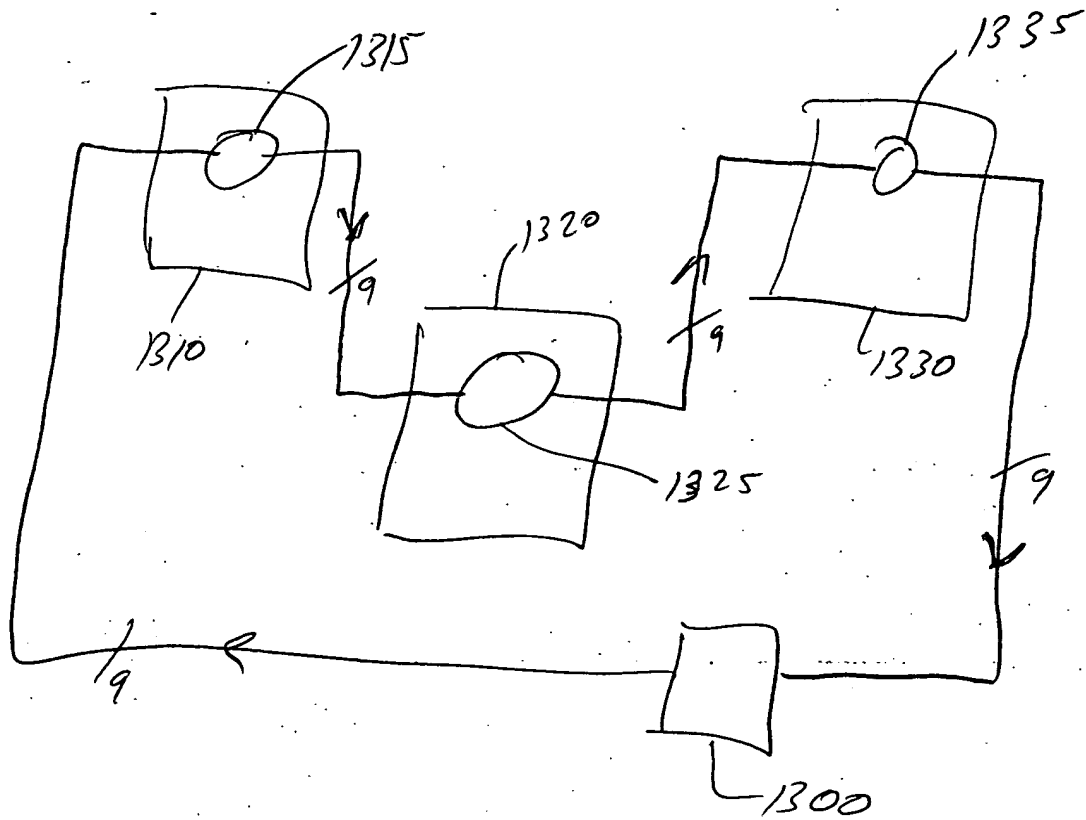


Fig. 13A

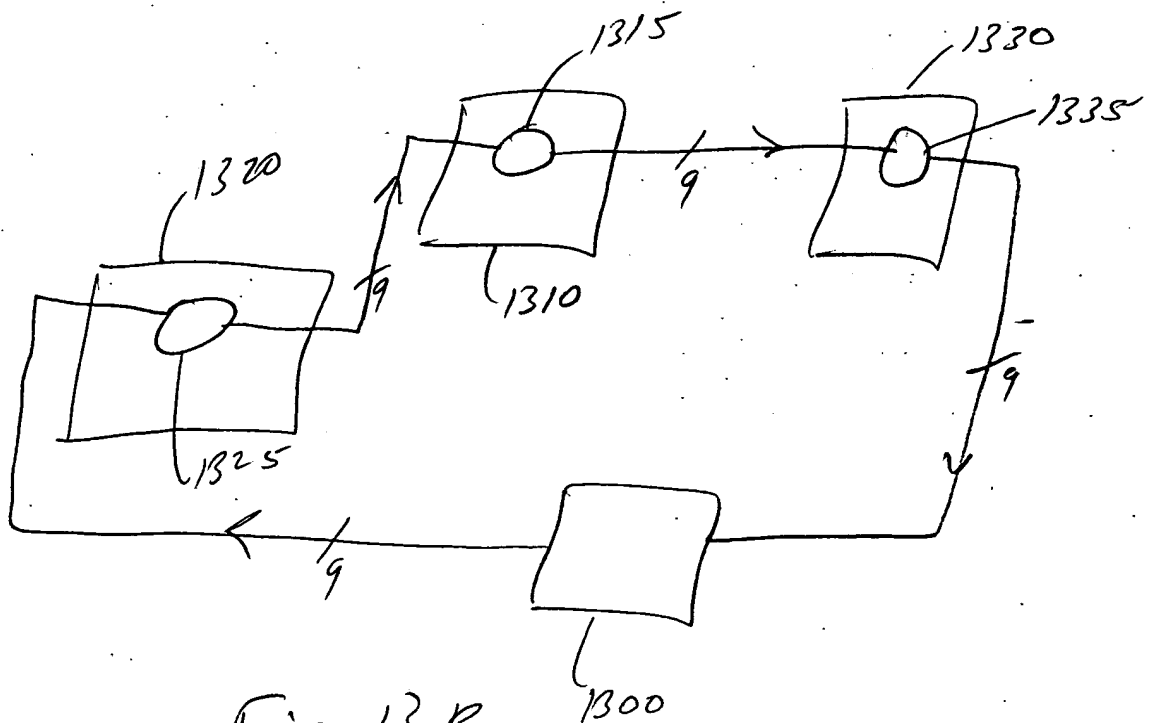


Fig. 13B